Dear Student

Broadcom (Avago Technologies) has an open position of VCSEL product engineer at III-V fab of Broadcom (Avago Technologies).

Please see below for the job responsibilities and requirements. Interested candidates, please send your resume to either at Broadcom website, or send resumes to rashit.nabiev@broadcom.com.

III-V VCSEL Device Product Engineer
Open position for VCSEL Product Engineer fin III-V fab in Singapore

**Job Responsibilities**

- Defining and maintaining VCSEL device performance, mechanical and quality spec’s
- Extensive data analysis for devices. Ex: correlation between epi, fab process, device layout with wafer device yields
- Test Yield reporting and targets
- Driving continues yield improvement and cost reduction
- III-V to module customer interface
- Working closely with RnD and process integration teams, including setting requirements for process changes that do or may impact device performance or reliability

**Requirements:**

- MS/PhD in electrical engineering, physics, or equivalent.
- 3+ years’ experience in Product or yield engineering, semiconductor optoelectronic device fabrication or development
- Proficiency in large data analysis, solid knowledge of statistical tools, like JMP - essential
- Strong analytical and communication skills
- Understanding of optoelectronic device operation and physics
- Experience in semiconductor laser/VCSEL or PIN applications a plus

Regards

**Pastoral Care and Career Advisory Office**

EEE Career Advisory

site: [http://www.eee.ntu.edu.sg/Programmes/CurrentStudents/sws/CareerAdvisory/Pages/Home.aspx](http://www.eee.ntu.edu.sg/Programmes/CurrentStudents/sws/CareerAdvisory/Pages/Home.aspx)