

Dear Class of 2014,

Maxim Integrated is currently recruiting for the following positions:

1. Entry-Level IC Design Engineer
2. Entry-Level Product Engineer

If you are interested, please submit your resume to Tiong.Lim@maximintegrated.com or rawinder.dharmalinggam@maximintegrated.com.

Maxim Integrated

Maxim Integrated is a highly successful, \$2.4 billion company. With offices and manufacturing sites around the world, we design award-winning semiconductors that make the world more integrated. We also know that it's our people who make us a great company. So we reward bold thinking, teamwork, personal growth, and community involvement. Want to make a difference and be challenged every day? Join us at Maxim Integrated. With analog integration, the possibilities are endless.

Maxim's Launch Program offers graduates and new talent within the industry the opportunity to gain hands-on professional experience within an innovative, global company.

New hires can expect:

- The opportunity to own projects and make an impact at a 9,000+ employee company
- Exposure to executive leadership, up to, and including, the CEO
- Competitive total rewards package (base + bonus + 13th month/AWS, stock options (for eligible positions), comprehensive benefits package, and generous time-off policy)
- Educational assistance programs

1. Entry-Level IC Design Engineer

Job Description:

Candidate will have immediate opportunities to work in a IC development project, within a successful IC Design team, from planning phase till the production and end of product life. The IC development team applies the most advance and innovative circuit design and validation flow to ensure best in class product and timely product delivery.

Responsibilities will grow to include:

- Work with other members of the design group in design and development of state of the art DC-DC converter products for next-generation integrated switching regulator ICs.
- Develop IPs including but not limited to Buck regulators, LDOs, Voltage and Current References, clock, bias circuitry.
- Process Selection, circuit specification and design, system modeling, top level simulation, corner simulation, documentation and design verification
- Provide guidelines to Mask designers on circuit layout and full-chip layout
- Develop and generate test plan and work with test engineers to test the IPs and product
- Participate the IPs and product evaluation/verification and debugging
- Participate in defining product specifications, concept and schedule
- Communicate and generate documents to support design cycle phases and align other functional team.

Job Requirements:

- BS in electrical and electronic engineering or higher
- Strong understanding of analog/mixed-signal/digital circuit design, device layout, CMOS device physics and basic semiconductor wafer processing
- Excellent problem-solving ability along with IC simulation and bench debugging skill
- Strong oral and written communication skills with ability to work effectively in interdisciplinary team environments
- Ability of take ownership, work independently, attention to details resulting in timely delivery
- Desire to learn new skillsets and apply critical thinking to a wide variety of technical issues

2. Entry-Level Product Engineer

Job Description:

Candidate will have immediate opportunities to utilize innovative problem solving skills to effectively segment and resolve key critical product issues that gate successful product qualification and introduction. Daily responsibilities will include owning and driving qualification and characterization activities, many of which will require collaboration with design, test, applications, and quality/reliability groups.

Responsibilities will grow to include:

- Product and package reliability tests (e.g. op life, ESD, latch-up, temp cycling and storage) and analyses
- Bench characterization as a function of temp, voltage, and load leading to product datasheet definition
- Statistical analysis of inline e-test, sort and final test data
- Product yield verification and optimization
- Closure of customer quality issues alongside internal quality & failure analysis groups

Job Requirements:

- BS in electrical engineering, materials science, applied physics or chemical engineering
- Coursework and/or knowledgebase in one or more of the following areas: CMOS device physics, basic semiconductor wafer processing, digital/analog/mixed-signal circuits
- Excellent problem-solving skills along with familiarity of basic statistics
- Strong oral and written communication skills with ability to work effectively in interdisciplinary team environments
- Desire to learn new skillsets and apply critical thinking to a wide variety of technical issues
- Experience with basic test bench equipment (e.g. voltage/current supplies, scope) and setups a plus
- Sponsorship not offered for this opportunity

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