

MediaTek Singapore

RF/Analog Design Engineer

Job Description

1. Design RF/Analog/Mixed-Signal IC circuit blocks / IPs based on specifications (performance, area, power).
2. Realize IC layout with floor planning and performance considerations (with layout engineers).
3. Testing & Debugging of IC prototypes (with system verification engineers).
4. Support of IC to mass production ready.
5. Continual support / debug of field related issues or customer IC rejects.
6. IC performance documentation

Minimum Requirements

- BEng or Masters
- Minimum 2 years experience

Interested personnel can send resumes to weeliang.lien@mediatek.com. We regret that only shortlisted candidates will be notified.