

Principal Engineer (System-on-Chip Verification)

Job Description:

- Technical lead responsible for system-on-chip (SoC) verification of Infineon Tricore-based microcontroller family in a team of SoC verification engineers
- Overall responsibility and ownership of new microcontroller SoC verification concept and verification plans, including support for and review of verification plans provided by the team. Verification content ownership based on specifications and application scenarios, ISO26262 Safety product requirements and SoC design requirements
- Technical coordination of SoC verification team: direction setting in terms of verification focus, debug and troubleshooting; training and coaching of fellow engineers
- Work in a multi-cultural environment with international (eg Europe) R&D interfaces: application and concept engineering, methodology, design and subsystem/IP verification teams, SW development, etc
- Handling test bench integration of SoC design in all phases of verification (RTL, gate-level), including centralized troubleshooting of design issues over wide range of IP components: digital IP, mixed signal IP simulation models, firmware, etc

Qualifications:

- MSc Degree in Electrical Engineering

Experience / Knowledge:

- At least 15 years experience in functional verification, especially SoC verification
- Very good understanding of processor (eg. ARM or other cores) based SoC architecture and SoC & IP designs to identify critical areas for focus of SoC verification
- Must have experience in Automotive microcontroller development/verification
- Preferably has background in verification/development of ISO26262 Safety products
- Excellent communication skills, verbal and written and strong leadership capability
- Embedded C and C++, SystemC, System Verilog, assertions (PSL, SVA), VHDL and Verilog, Unix scripting (Perl, Shell)