Senior Staff Engineer/Staff Engineer, SoC Implementation

Job Description:

- Working with the team of SOC implementation engineers.
- Fully responsible for working in tapeouts of complex designs on various technology nodes and providing valuable suggestion for best physical implementation.
- Worked on full chip designs both flat and hierarchical and driven the sub-block owners for design closure.
- Interacting with design, verification and DFT teams across all sites of Infineon - understand and adapt to the current Infineon implementation flow.
- Working with counterparts in other sites to jointly execute projects as an integral part of the team in global environment.
- Help, assist, resolve technical issues with team members - grow the SOC implementation team to take up multiple projects in parallel.
- Ability to lead projects and ensure team members deliver with high quality.
- Proactively work with expert team to drive improvements in implementation methodology.

Qualifications:

- Degree/Masters/PhD in Electrical/Electronic Engineering or related fields from and accredited institution.

Experience / Knowledge:

- 8 – 10+ yrs of Physical Design experience.
- Worked on multiple tapeouts at ultra-deep sub-micron technologies, 65nm/40nm and smaller nodes, in complex multi-million gate designs.
- Experience in Automotive and Industrial domain designs is desirable.
- Strong technical knowledge in microelectronics and/or system architectures.
- Should have hands on experience in complete RTL to GDSII flow, which includes synthesis, DFT, IO planning, floorplanning, power analysis, P&R, CTS, extraction, timing closure, physical verification, rail analysis etc.,
- Strong experience in timing closure, constraining and timing analysis required.
- Excellent debugging skills to diagnose and devise workarounds for design issues.
- Knowledge on package design, ESD, integration of analog and mixed signal macros is an advantage.
- Experience in low power/multi voltage design and understanding of UPF is a must.
- Deep knowledge of Synopsys or Cadence implementation tools and flow.
- Perl and TCL/TK required to achieve highly automated, reproducible and fast results.
- Knowledge about data management is a definite plus.
- Good team player working with geo-dispersed cross cultural and cross functional teams.
- Good communication and interpersonal skills required. Fluent in English.