

NM6015 Embedded Systems

Compositional Timing Analysis; Worst-Case Execution Time (WCET) Analysis; Realtime Programming Language (Esterel,Lustre); Microprocessor Architecture (RISC Vs DSP, VLIW, SIMD, FPGAs); Software Platforms, Tools for Host-Target-Development; Monitoring and Debugging; Simulation& Design Space Exploration; RTOS and Scheduling Policies; Processor Clock Rate, Benchmarking, Memory Hierarchy (Cache)