Job Title:

➢ Video Encoding Algorithm and Image Signal Processing Algorithm Experts

Key Responsibilities:

➢ Participate in research, and development of advanced image and video processing algorithms for multimedia and communication applications

Required Skills:

➢ Research in video encoding algorithms, such as motion estimation, rate control, encoding mode decisions, etc.

➢ Perform research into leading edge and emerging video technologies, ideas, approaches and keep up to date with new video processing standards and general industry trends

➢ Algorithm enhancement for video quality and performance tuning on the silicon environment.

➢ C-model simulation for video compression algorithm development and verification.

➢ Ability to express ideas and communicate effectively.

➢ Any of the following skills and experiences would be an added advantage:
  o Video algorithms for pre-processing, post-processing, video sharpening, temporal/spatial noise reduction, etc.
  o Key image processing algorithms for next generation Camera Signal Processing cores

Required Qualifications:

➢ PhD or Master’s degree (PhD Preferred), fresh PhD graduates are welcome to apply.

➢ Insightful know-how on video compression algorithms, such as HEVC (HM/x265), H264 (JM/x264), VP9 (libvpx), VP8 (libvpx), VC1, MPEG4, MPEG2, etc, or

➢ PC-based algorithmic image signal processing (ISP) development for projects, including both image quality enhancement and machine vision applications

➢ Implementation of ISP algorithms on embedded DSP platforms with associated CPU and memory bandwidth limitations

➢ Working knowledge of C/C++ and/or Matlab and proficiency with DSP and filter design.

➢ Familiarity with ASIC design approach and environment