EE6601       ADVANCED WAFER PROCESSING

Acad Unit:  3
Pre-requisite:  NIL
Effective:  AY2014-2015 Semester 2
Last update:  10 October 2013

OBJECTIVE

1. To study deep sub-micron front end process technology
2. To study deep sub-micron back end process technology
3. To study characterization techniques relevant to deep sub-micron process technology

LEARNING OUTCOME

The students will be exposed to state-of-the-art advanced CMOS process technologies. They will also be exposed to future technology. They will also become more familiar with the relevant diagnostic techniques for process related issues.

OTHER RELEVANT INFORMATION

Prior knowledge required: some basic knowledge of MOSFETs and CMOS technology
Level of difficulty: medium
Mathematics: simple

CONTENT


ASSESSMENT SCHEME

Continuous Assessment: 20%
Final Examination: 80%

TEXTBOOKS

REFERENCES

